## **Electronic Information Disclosure Statement**

# HIGH PERFORMANCE VERTICAL PNP TRANSISTOR AND METHOD

Application:

Confirmation:

Applicant(s):

Peter Gray\_

Docket

BUR920010190

Number: Group Art

Unit:

Examiner:

(4694562 or 4868135 or 5066602 or 5117271 or 5175606 or 5196356 or 5248624 or 5302534 or

search string: 5407841 or 5407847 or 5525530 or 5541125 or 5691226 or 5672897 or 5677558 or 5702959 or

5789288).pn.

#### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

llinit	Citation No.	Patent Number	Date	Bar Code	Patentee	Class	Subclass
M	P01	4694562	1987-09-22		lwasaki et al.	437	57
	P02	4868135	1989-09-19		Ogura et al.	437	31
	P03	5066602	1991-11-19		Takemoto et al.	437	31
	P04	5117271	1992-05-26		Comfort et al.	357	34
	P05	5175606	1992-12-29		Tsai et al.	257	320
	P06	51.96356	1993=03-23		Won-et-al	437_	3.1
W	P07	5248624	1993-09-28		Icel et al.	437	31

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lma	P08	5302534	1994-04-12		Monk et al.	437	31
	P09	5407841	1995-04-18		Liao, et al.	437	31
	P10 .	5407847	1995-04-18		Hayden et al.	437	44
	P11	5525530	1996-06-11		Watabe	437	34
	P12	5541125	1996-07-30		Williams et al.		
	P13	5691226	1997-11-25		Foerstner et al.	437	59
	P14	5672897	1997-09-30		Watanabe et al.	257	370
	P15	5677558	1997-10-14		McGlinchey	257	370
	P16	5702959	1997-12-30		Hutter et al.	437	31
M	P17	5789288	1998-08-04		Palmieri et al.	438	234

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W	P17	5789288	1998-08-04	1888   1881   1881   1882   1882   1883	Palmieri et al.	438	234

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Examiner Name	Date
	3/2/04